



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	David Hartwell	Examiner:	Nitin C. Patel
Serial No.:	10/649,523	Group Art Unit:	2116
Filed:	August 27, 2003	Docket No.:	200301890-2
Title:	Clock Forward Initialization and Reset Signaling Technique		

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop: Issue Fee  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

Please enter the following Response to Examiner's Reasons for Allowance.  
 Remarks begin on page 2.

**AUTHORIZATION TO DEBIT ACCOUNT**

It is believed that no extensions of time or fees for net addition of claims are required, beyond those, which may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, such extensions are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefore (including fees for net addition of claims) are hereby authorized to be charged to Hewlett-Packard Development Company's deposit account no. 08-2025.